



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Martin Gröpl

Serial No.: 10/652,256

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Title: INTEGRATED CIRCUIT WITH OVERVOLTAGE PROTECTION

Docket: 36088

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.97 and 1.98, Applicant is submitting herewith Form PTO-1449 listing references for consideration by the Examiner. Also submitted herewith is a legible copy of each non-U.S. reference. The undersigned hereby certifies that each item of information contained in this Supplemental Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Supplemental Information Disclosure Statement.

Respectfully submitted,

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Date: 2-26-04

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

John P. Murtaugh
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2-26-04 John P. Murtaugh
Date Signature of Attorney

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INFORMATION DISCLOSURE CITATION
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(USE SEVERAL SHEETS IF NECESSARY)

APPLICANT:
Martin GröplFILING DATE:
August 29, 2003

GROUP ART UNIT:

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Date	Name	Class	Subclass	Filing Date If Appropriate
	A						
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FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Class	Subclass	Translation
	L	DD 281 331 A7	8/1990	Germany			English Abstract and Comment
	M						
	N						
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

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